Roll No ....

IT-503(B)-CBGS

B.Tech., V Semester

Examination, June 2020

Choice Based Grading System (CBGS)

Microprocessor and Interfacing

Time : Three Hours

Maximum Marks : 70

Note: i) Attempt any five questions.

ii) Each questions carry equal marks.

iii) In case of any doubt or dispute the English version question should be treated as final.

1. a) Write about the evolution of Microprocessors. 7

    Microprocessors Ho?

   b) What is the Interface? Explain input and output interface in detail. 7

    Interface

2. a) What is the Assembler? Explain the directives of assembler. 7

    Assembler Assembler

   b) Explain the register organization of 8086. 7

    8086 register organization

IT-503(B)-CBGS PTO

3. a) Explain 8255 programmable interface in detail. 7

   8255 programmable interface

   b) Explain Memory interfacing in detail. 7

   Memory interfacing

4. a) What is Memory Map? Explain it with diagram. 7

   Memory Map

   b) What is Addressing Mode? Explain. 7

   Addressing Mode

5. a) Draw the IC pin diagram of 8085 microprocessor and explain function of different pins. 7

   8085 microprocessor

   b) How demultiplexing process of address/data bus takes place in 8085 explain? 7

   8085 address/data bus Demultiplexing

6. a) Differentiate max and min mode of 8086 microprocessor.7

   8086 microprocessor

   b) Draw internal functional diagram of 8255 programmable interface and explain its working. 7

   8255 programmable interface

IT-503(B)-CBGS   PTO

7. a) Explain different mode of operations for 8254 timer with suitable wave form diagram.   7

   8254

   b) Explain the BSR mode of 8255 PPI.   7

   8255

8. Explain the following terms with example :   14
   a) Addressing Modes
   b) DMA
   c) USB
   d) EPROM

******